HEPIX Summary (Part 3)

Spring meeting 2006 Caspur, Rome

S.Jarp, IT/CERN

Some sessions which I found interesting and representative (and I hope you will)

Tuesday morning

• Batch:

10:30 Batch Systems SIG (Convener: Tony Cass/CERN)

- Passing information to the CE (gLite) (Francesco Prelz/INFN)

- Interfacing BLAHP with LSF Status Report (Ulrich Schwickerath/CERN)
- -11.30 Batch Systems SIG, contd (Convener: Tony Cass/CERN)
- Experiment plans for batch system use (ATLAS) (Laura Perini/INFN)
- Experiment plans for batch system use (CMS) (Stefano Belforte/INFN)
- Experiment plans for batch system use (LHCb) (Andrei Tsaregorodtsev/CPPM)
- Experiment plans for batch system use (ALICE) (Federico Carminati/CERN)
- Conclusion & discussion (Tony Cass/CERN)

Wednesday part 1

• Keynote + Optimisation:

09:00 Plenary HEPiX/GDB talk

- Key challenges for Computer Centre Managers supporting LHC computing Speaker: Les Robertson/CERN

09:30 Optimisation and bottlenecks (Convener: Wojciech Wojcik/IN2P3)

- Understanding and addressing performance issues in HEP (Sverre Jarp/CERN)
- Code/compiler problems and how to reach an improvement (Rene Brun/CERN)
- Usage of BQS resources to control bottlenecks upstream (Julien Devemy/IN2P3)

11:30 **Optimisation and bottlenecks, contd** (Convener: Wojciech Wojcik/IN2P3)

- Optimisation of dCache and DPM (Greig Cowan/U.Edinburgh)
- Conclusions and future plans (Wojciech Wojcik/IN2P3)

LCG - The Worldwide LHC Computing Grid

LHC Data Analysis

Challenges for 100 Computing Centres in 20 Countries

HEPiX Meeting Rome 5 April 2006

Les Robertson LCG Project Leader





LCG Service Deadlines





Conclusions

LCG will depend on

- ~100 computer centres run by you
- two major science grid infrastructures EGEE and OSG
- excellent global research networking
- We have
 - understanding of the experiment computing models
 - agreement on the baseline services
 - good experience from SC3 on what the problems and difficulties are
- Grids are now operational
 - ~200 sites between EGEE and OSG
 - Grid operations centres running for well over a year
 - > 20K jobs per day accounted
 - ~15K simultaneous jobs with the right load and job mix

BUT - a long way to go on reliability



- The Service Challenge programme this year must show that we can run reliable services
- Grid reliability is the product of many components

 middleware, grid operations, computer centres,





Experiment plans for batch system usage

Federico Carminati HEPiX Rome, April 4, 2006

ALICE computing model

- For pp similar to the other experiments
 - Quasi-online data distribution and first reconstruction, calib and alignment at T0; prompt analysis @CAF
 - Further reconstructions at T1's
- For AA different model
 - Calibration, alignment, pilot reconstructions, prompt analysis@CAF and partial data export during data taking
 - Data distribution and first reconstruction at T0 in the four months after AA run (shutdown)
 - Further reconstructions at T1's
- T0: First pass reconstruction, storage of RAW, calibration data and first-pass ESD's
- T1: Subsequent reconstructions and scheduled analysis, storage of a collective copy of RAW and one copy of reconstructed and simulated data to be safely kept, disk replicas of ESD's and AOD's
- T2: Simulation and end-user analysis, disk replicas of ESD's and AOD's

Job submission

- Job agents
 - Sent only when needed
 - Avoid waste of resources and "useless" updates of the ALICE Job Catalogue
 - Eliminate "black hole" effect
- Job location determined by the data location
- WN outbound connectivity required
 We are working on removing this constraint
- System used for large production
 - 22,500 jobs, 540 KSi2K hours, 20TB
 - 2.5% inefficiency thanks to job agents

Batch systems use in ALICE

- Past use
 - Through AliEn use of all flavours of batch schedulers (LSF, PBS, BQS, SGE, Condor) at many computing centres worldwide
 - Few separate queues for different jobs types
 - Job priorities handled in the central TQ
- Present status
 - Practically no direct access to batch queues: shielded by the GRID (LCG, OSG, ARC) CE
 - Middleware is increasingly 'taking away' the functions of the batch systems (job prioritization based on job length, queuing)
 - Fewer users submit jobs locally: ultimately all offline computing tasks in ALICE will be performed on the GRID (production, calibration, analysis), users will submit all jobs to the GRID interface

ALICE requirements



- We see the interaction with the batch systems (specific submission commands, error handling and reporting, log and output files, etc...) as part of the GRID service
 - Therefore we have no special preferences to the type of batch systems deployed at the sites
- Connected with this we still do not have a properly secured sandbox
- For that we would probably need Job Agent to
 - Start virtual machine
 - ...or start another process under different user id using glexec/sudo mechanism
- However this is not a show-stopper for us

ALICE requirements



- From practical point of view, presently we require
 - One single long ALICE-specific queue
 - Would like a uniform publishing of queue length in kSI2k•h (ultimately also a GRID function) across sites
 - Ability to guarantee the share of computing resources for ALICE
 - Ability to specify the amount of memory needed by a job
 - A minimum memory requirement of 2 Gb per core
 - Scratch space of several GB
 - A shared home directory for software installatinos etc..



Tier-2 optimisation of dCache and DPM





A 'typical' Tier-2

No such thing as 'typical', but there are some commonalities, i.e.

- Limited hardware resources:
 - One or two nodes attached to a few TB of RAID'ed disk.
 - Some storage NFS mounted from another disk server.
 - No tape storage.
- Limited manpower to spend on administering/configuring an SRM.
- Choice of SRM solutions (dCache, DPM, StoRM ...)
- Require the SRM they choose to be optimised in order to be able to handle the data flows that are expected when the LHC comes online.
 - GridPP service challenge set target that all T2s should be able to sustain T1 \rightarrow T2 transfer rate of \geq 300Mb/s.

HEPiX April 2006



Best results observed with xfs + SLC 3.0.6 and a 2.6 kernel. FTS parameters: Nf \sim 10, Ns = 1.

Greig A Cowan

Plea for assistance so that Tier2 centres can benefit from existing knowledge in this area \rightarrow P.Kelemen's talk

Some hints to improve compilation time and execution performance (René Brun)

2006

Time to compile

- May be a problem in some experiments
- Some recipes for improvement
- Shared libs

Improving the execution time

- Code inlining (good and bad aspects)
- Using the right collection classes
- Profiling tools
- Differences between compilers or compiler versions
- Ready for Multithreading

Example with smatrix

	TestKalman [nx,ny] : kalman_win7.1								
	2	3	4	5	6	7	8	9	10
2	<mark>0.41</mark>	0.55	0.80	1.26	2.16	3.91	5.70	7.43	9.66
	0.40	0.56	0.79	1.31	2.36	3.84	5.14	6.97	8.90
	1.01	1.29	1.65	2.18	3.16	7.13	8.85	11.24	13.66
3	0.52	0.70	0.98	1.49	2.43	4.35	6.07	8.23	10.09
	0.51	0.70	0.98	1.52	2.62	4.15	5.46	7.55	9.17
	1.24	1.50	1.97	2.61	3.68	7.82	9.53	12.26	14.95
4	0.63	0.85	1.24	1.73	2.79	4.77	6.65	8,64	10.86
	0.62	0.85	1.17	1.77	2.86	4.46	5.93	7.93	10.01
	1.50	1.88	2.19	3.09	4.31	8.53	10.56	13,77	16.58
5	0.78	1.04	1.41	2.11	3.12	5.12	7.17	9,64	11.45
	0.83	1.09	1.45	2.10	3.22	4.90	6.53	8.70	10.56
	1.81	2.24	2.91	3.49	5.02	9,40	11.56	14,88	17.61
6	0.85	1.16	1.68	2.28	3.50	5.57	8.12	9,94	12.50
	0.98	1.29	1.72	2.49	3.72	5.44	7.07	9,22	11.42
	2.13	2.65	3.40	4.37	5.49	10.36	12.53	16,09	19.24
7	1.04	1.50	2.01	2.79	4.03	6.24	8.48	10.76	13.30
	1.10	1.48	1.99	2.80	4.15	5.89	7.64	9.80	11.96
	2.44	3.09	3.95	4.95	6.47	10.88	13.44	17.59	20.76
8	1.22	1.69	2.30	3.18	4.59	6.89	9.24	11.67	14.35
	1.26	1.71	2.30	3.16	4.57	6.47	8.69	10.78	13.03
	2.81	3.57	4.48	5.57	7.28	12.02	14.23	18.69	22.77
L	N1,N	2 <= 6	36.51 37.96 66.81	N1,N	2 > 6	261.15 242.13 421.54	All N	1,N2	297.67 280.08 488.35
SMatrix_Sym			SMatrix	ТМа	trix	SMatrix	_Sym bel	ter than	TMatrix

	TestKalman [nx,ny] : kalman_solaris.5.9					5.9			
	2	3	4	5	- 6	7	8	9	10
2	2.29	4.53	7.49	13.36	27.92	30.68	42.12	56.27	74.79
	1.39	2.41	3.52	5.57	9.88	20.13	29.90	41.89	56.08
	2.49	2.95	3.84	5.15	8.18	34.74	42.52	51.08	61.38
3	3.36	6.28	9.88	17.60	33.32	37.58	51.27	69.29	88.88
	2.05	3.43	5.09	7.79	12.81	24.26	35.10	50.11	66.09
	2.83	3.49	4.74	6.23	9.61	36.25	44.61	53.69	63.78
4	4.70	8.39	13.02	21.30	38.09	44.86	62.55	83.96	108.25
	2.92	4.82	7.16	10.45	16.27	28.23	43.15	60.94	79.72
	3.50	4.41	5.68	7.46	11.42	38.35	47.23	56.35	67.32
5	6.45	11.09	16.75	26.01	45.35	52.92	73.96	100.57	127.69
	3.84	6.42	9.42	13.43	20.22	32.57	50.84	72.06	94.24
	3.87	5.10	6.78	8.88	12.96	40.86	49.51	59.60	70.80
6	8.77	14.55	21.27	32.27	51.35	63.23	87.57	118.44	152.52
	5.36	8.67	12.45	17.49	25.37	39.55	60.54	84.29	112.31
	4.58	6.12	8.33	10.55	14.90	43.39	52.76	63.18	75.01
7	12.58	20.21	29.16	42.12	64.82	78.81	107.49	142.03	183.05
	6.85	10.88	15.45	21.34	29.96	44.96	68.27	96.24	128.52
	5.27	7.13	9.41	12.36	17.47	45.91	55.53	66.99	79.38
8	17.68	28.33	40.40	57.23	84.57	103.45	139.67	184.39	232.32
	10.79	17.19	24.55	33.55	46.08	64.54	95.46	132.12	170.91
	6.08	8.30	10.98	14.26	19.58	48.60	58.98	70.57	83.94
	N1,N	12 <= 6	445.38 218.23 164.08	N1,N	2 > 6	3095.72 2099.65 1673.16	All N	11,N2	3541.10 2317.89 1837.24
SMatrix_Sym S			SMatrix	TMa	atrix	SMatrix	_Sym be	tter than	TMatrix

2006

	Tes	tKaln	nan [r	ıx,ny]	: ka	lman_	_slc3	_gcc:	323
	2	3	4	5	6	7	8	9	10
2	0.30	0.38	0.56	0.88	1.54	5.77	8.00	10.41	14.36
	0.33	0.44	0.64	0.98	1.64	5.84	7.81	10.23	14.58
	0.86	1.00	1.39	1.69	2.96	5.22	6.06	7.41	9.03
3	0.37	0.56	0.72	1.10	1.84	6.24	<mark>8.19</mark>	10.97	14.37
	0.46	0.60	0.99	1.29	2.03	6.33	8.17	10.76	14.02
	1.01	1.16	1.59	1.96	3.40	5.48	6.64	7.61	9.86
4	0.47	0.63	0.89	1.39	2.16	6.71	9.04	11.71	15.07
	0.61	0.76	1.03	1.48	2.27	6.43	8.92	11.37	14.69
	1.16	1.42	1.72	2.48	3.67	6.14	6.95	8.85	10.33
5	0.60	0.85	1.19	1.71	2.58	7.03	9.52	<mark>12.41</mark>	15.74
	0.78	1.03	1.28	1.80	2.70	6.79	9.18	12.03	16.00
	1.28	1.55	2.35	2.69	4.44	6.60	8.34	9.44	12.16
6	0.77	1.26	1.49	2.13	3.06	7.81	10.19	12.98	17.56
	0.96	1.22	1.60	2.17	3.12	7.39	9.90	12.53	16.92
	1.59	2.09	2.42	3.58	4.61	7.55	8.09	10.36	11.58
7	0.96	1.33	1.77	2.46	3.47	8.24	10.56	13.08	18.03
	1.25	1.49	1.99	2.57	3.62	8.08	10.13	12.72	16.96
	1.75	2.17	3.03	3.53	5.48	7.90	9.50	10.62	14.14
8	1.14	1.68	2.15	2.95	4.07	8.99	11.47	14.48	19.15
	1.48	1.79	2.33	3.14	4.27	8.79	11.37	14.36	18.07
	2.05	2.81	3.02	4.67	5.59	8.69	9.34	12.29	13.71
N1,N2 <= 6		29.45 32.23 54.07	N1,N2	2 > 6	340.08 334.29 283.99	All N	1,N2	369.53 366.52 338.05	
SMatrix_Sym SMatr		SMatrix	TMatrix		SMatrix_Sym better than *			TMatrix	

René Brun, CERN

Some hints to improve performance

MultiCore: Impact on ROOT

- There are many areas in ROOT that can benefit from a multi core architecture. Because the hardware is becoming available on commodity laptops, it is urgent to implement the most obvious asap.
- Multi-Core often implies multi-threading. There are several areas to be made not only thread-safe but also thread aware.
 - PROOF obvious candidate. By default a ROOT interactive session should run in PROOF mode. It would be nice if this could be made totally transparent to a user.
 - Speed-up I/O with multi-threaded I/O and read-ahead
 - Buffer compression in parallel
 - Minimization function in parallel
 - Interactive compilation with ACLIC in parallel
 - etc..

Performance and Bottleneck Analysis (S.Jarp/R.Jurga)

Typical profile Stop press: G4ATLAS

Samples	Self %	Total %	Module	
11767458	36.64%	36.64%	libG4geometry.so	
5489494	17.09%	53.73%	libG4processes.so	
2283674	7.11%	60.85%	libG4tracking.so	
2146178	6.68%	67.53%	libm-2.3.2.so	
2057144	6.41%	73.93%	libstdc++.so.5.0.3	
1683623	5.24%	79. 18%	libc-2.3.2.so	
933872	2.91%	82.08%	libCLHEP-GenericFunctions-1.9.2.1.so	
685894	2.14%	84.22%	libG4track.so	
655282	2.04%	86.26%	libCLHEP-Random-1.9.2.1.so	
524236	1.63%	87.89%	libpthread-0.60.so	
283521	0.88%	88.78%	libCLHEP-Vector-1.9.2.1.so	
265656	0.83%	89.60%	libG4materials.so	
205836	0.64%	90.24%	libG4Svc.so	
197690	0.62%	90.86%	libG4particles.so	
190272	0.59%	91.45%	ld-2.3.2.so	
150757	0.47%	91.92%	libCore.so (ROOT)	
149525	0.47%	92.39%	libFadsActions.so	
126111	0.39%	92.78%	libG4event.so	
123206	0.38%	93.16%	libGaudiSvc.so	



G4Atlas simulation (3 events)





Total instructions

Cycles	6252 * 10^9
Total inst	2136 * 10^9
TOT INS/CYC	0.342 (0.684 on one CPU)

Floating-point instructions

FP	397 * 10^9
FP/TOT	0.186

LD/ST/BR instructions

LD	814 * 10^9
LD/TOT	0.38
L2LM	60 * 10^9
L2LM/LD	0.074

ST	528 * 10^9
ST/TOT	0.247
L2SM	0.60 * 10^9
L2SM/ST	0.00113

BR_TP	218 * 10^9
BR_TM	5.4 * 10^9
BR_TP/TOT	0.097
BR_TM/TOT	0.00252

